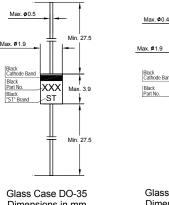
Silicon Epitaxial Planar Switching Diode

Applications

· High-speed switching

This diode is also available in MiniMELF case with the type designation LL4148



Glass Case DO-34 Dimensions in mm Dimensions in mm

Absolute Maximum Ratings (T_o = 25 °C)

Parameter		Symbol	Value	Unit
Peak Reverse Voltage		V_{RM}	100	V
Reverse Voltage		V_R	75	V
Average Rectified Forward Current		I _{F(AV)}	200	mA
Non-repetitive Peak Forward Surge Current	at t = 1 s at t = 1 ms at t = 1 µs	I _{FSM}	0.5 1 4	Α
Power Dissipation		P _{tot}	500 ¹⁾	mW
Junction Temperature		Tj	200	°C
Storage Temperature Range		T _{stg}	- 65 to + 200	°C

¹⁾ Valid provided that leads at a distance of 8 mm from case are kept at ambient temperature.









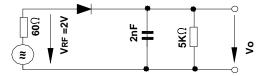


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Characteristics at T_a = 25 °C

Parameter	Symbol	Min.	Max.	Unit
Forward Voltage at I _F = 10 mA	V _F	-	1	V
Leakage Current at V_R = 20 V at V_R = 75 V at V_R = 20 V, T_j = 150 °C	I _R I _R	- - -	25 5 50	nA µA µA
Reverse Breakdown Voltage at I_R = 100 μ A at I_R = 5 μ A	V _{(BR)R} V _{(BR)R}	100 75	- -	V V
Capacitance at $V_R = 0$, $f = 1$ MHz	C_{tot}	-	4	pF
Voltage Rise when Switching ON tested with 50 mA Forward Pulses tp = 0.1 s, Rise Time < 30 ns, fp = 5 to 100 KHz	V _{fr}	-	2.5	V
Reverse Recovery Time at I_F = 10 mA to I_R = 1 mA, V_R = 6 V, R_L = 100 Ω	t _{rr}	-	4	ns
Thermal Resistance Junction to Ambient Air	R _{thA}	-	0.35 1)	K/mW
Rectification Efficiency at f = 100 MHz, V _{RF} = 2 V	ην	0.45	-	-

¹⁾ Valid provided that leads at a distance of 8 mm from case are kept at ambient temperature.



Rectification Efficiency Measurement Circuit





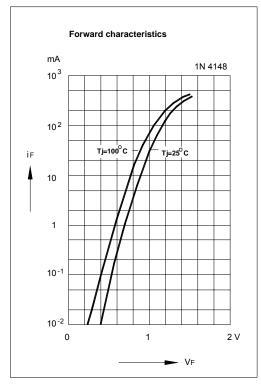


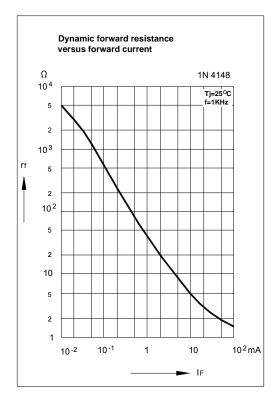


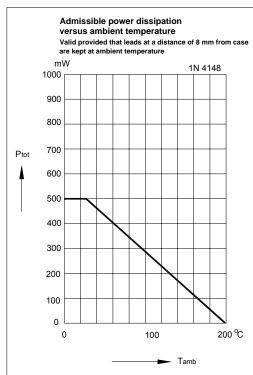


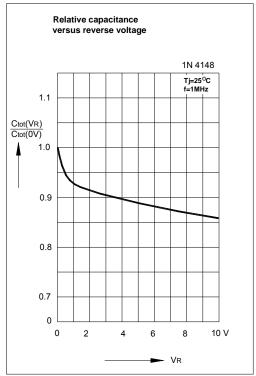


Dated: 15/06/2009











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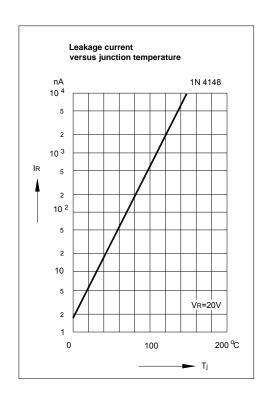


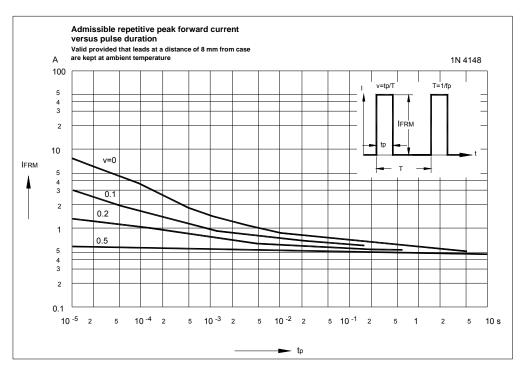














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