RENESAS

HD74LS153

Dual 4-Line to 1-Line Data Selectors / Multiplexers

REJ03D0439-0200 Rev.2.00 Feb.18.2005

This data selector / multiplexer contains inverters and drivers to supply fully complementary, on-chip, binary decoding data selection to the AND-OR-INVERT gates. Separate strobe inputs are provided for each of the two four-line sections.

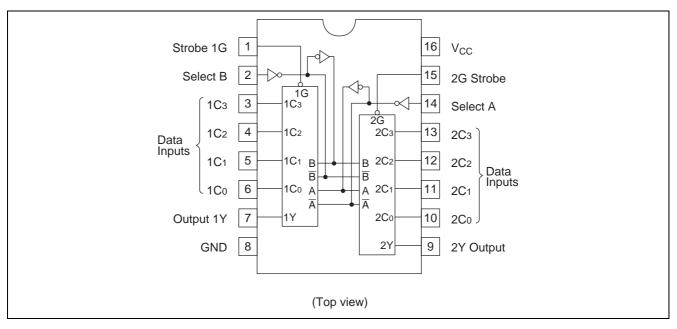
Features

• Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LS153P	DILP-16 pin	PRDP0016AE-B (DP-16FV)	Р	—
HD74LS153FPEL	SOP-16 pin (JEITA)	PRSP0016DH-B (FP-16DAV)	FP	EL (2,000 pcs/reel)
HD74LS153RPEL	SOP-16 pin (JEDEC)	PRSP0016DG-A (FP-16DNV)	RP	EL (2,500 pcs/reel)

Note: Please consult the sales office for the above package availability.

Pin Arrangement



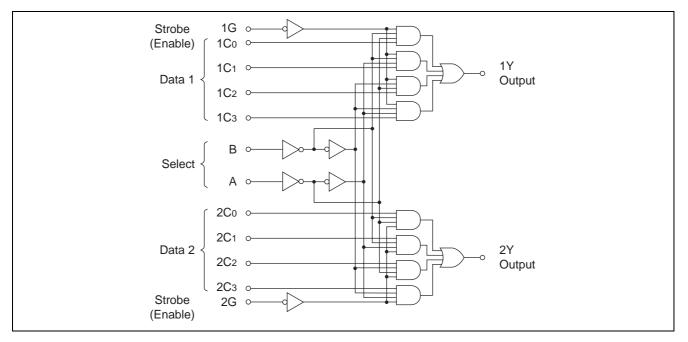


Function Table

	Inputs								
Se	lect		Data Strobe						
В	Α	C0	C1	C2	C3	G	Y		
Х	Х	Х	Х	Х	Х	Н	L		
L	L	L	Х	Х	Х	L	L		
L	L	Н	Х	Х	Х	L	Н		
L	Н	Х	L	Х	Х	L	L		
L	Н	Х	Н	Х	Х	L	Н		
Н	L	Х	Х	L	Х	L	L		
Н	L	Х	Х	Н	Х	L	Н		
Н	Н	Х	Х	Х	L	L	L		
Н	Н	Х	Х	Х	Н	L	Н		

H ; high level, L ; low level, X ; irrelevant

Block Diagram



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit
Supply voltage	V _{cc}	7	V
Input voltage	V _{IN}	7	V
Power dissipation	P _T	400	mW
Storage temperature	Tstg	–65 to +150	°C

Note: Voltage value, unless otherwise noted, are with respect to network ground terminal.

Recommended Operating Conditions

ltem	Symbol	Min	Тур	Max	Unit
Supply voltage	V _{CC}	4.75	5.00	5.25	V
Output current	I _{ОН}	—	—	-400	μΑ
Oupur current	I _{OL}	—	_	8	mA
Operating temperature	Topr	-20	25	75	°C



Electrical Characteristics

 $(Ta = -20 \text{ to } +75 \ ^{\circ}\text{C})$

Item	Symbol	min.	typ.*	max.	Unit	Condition
	V _{IH}	2.0	—		V	
Input voltage	V _{IL}	—	—	0.8	V	
	V _{OH}	2.7	—		V	$\label{eq:VCC} \begin{array}{l} V_{CC} = 4.75 \ V, \ V_{IH} = 2 \ V, \ V_{IL} = 0.8 \ V, \\ I_{OH} = -400 \ \mu A \end{array}$
Output voltage	V _{OL}	—	—	0.4	V	$I_{OL} = 4 \text{ mA}$ $V_{CC} = 4.75 \text{ V}, \text{ V}_{IH} = 2 \text{ V},$
		—	—	0.5		I _{OL} = 8 mA V _{IL} = 0.8 V
	IIH	—	—	20	μA	$V_{CC} = 5.25 \text{ V}, \text{ V}_{I} = 2.7 \text{ V}$
Input current	I _{IL}	—	—	-0.4	mA	$V_{CC} = 5.25 \text{ V}, \text{ V}_{I} = 0.4 \text{ V}$
	Iı	_	—	0.1	mA	$V_{CC} = 5.25 \text{ V}, \text{ V}_{I} = 7 \text{ V}$
Short-circuit output current	l _{os}	-20	_	-100	mA	V _{CC} = 5.25 V
Supply current**	Icc		6.2	10	mA	V _{CC} = 5.25 V
Input clamp voltage	VIK		—	-1.5	V	$V_{CC} = 4.75 \text{ V}, I_{IN} = -18 \text{ mA}$

Notes: * $V_{CC} = 5 V$, Ta = 25°C

 ** I_{CC} is measured with all outputs open and all inputs grounded.

Switching Characteristics

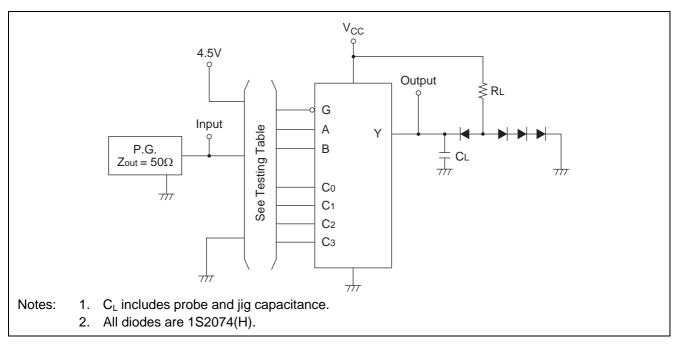
$(V_{CC} =$	5	V	$T_{2} -$	25°C	١
$(V_{CC} -$	J	۰,	1 a –	25 C	,

							(,	(, Iu = 25 C)
Item	Symbol	Inputs	Outputs	min.	typ.	max.	Unit	Condition
	t _{PLH}	Data	Y	—	10	15	ns	
	t _{PHL}	Data	Y	—	17	26	ns	
Propagation dolay time	t _{PLH}	Select	Y	—	19	29	ns	$C_L = 15 \text{ pF},$
Propagation delay time	t _{PHL}	Select	Y	—	25	38	ns	$R_L = 2 \ k\Omega$
	t _{PLH}	Strobe	Y	—	16	24	ns	
	t _{PHL}	Strobe	Y	_	21	32	ns	



Testing Method

Test Circuit

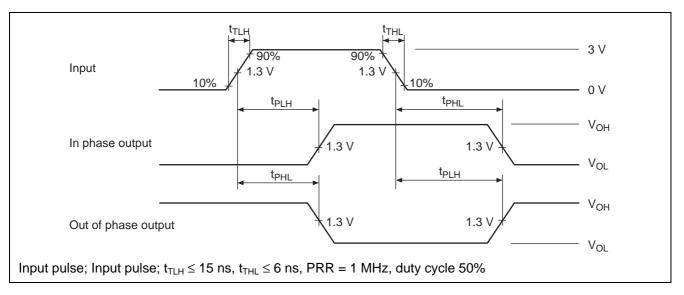


Testing Table

ltem		Inputs										
	В	Α	C ₀	C ₁	C ₂	C ₃	G	Y				
	GND	GND	IN	Х	Х	Х	GND	OUT				
	GND	4.5 V	Х	IN	Х	Х	GND	OUT				
	4.5 V	GND	Х	Х	IN	Х	GND	OUT				
	4.5 V	4.5 V	Х	Х	Х	IN	GND	OUT				
t _{PLH}	GND	IN	GND	4.5 V	Х	х	GND	OUT				
t _{PHL}	GND		4.5 V	GND								
	IN	GND	GND	х	4.5 V	х	GND	OUT				
		GND	4.5 V	^	GND	^						
	GND	GND	4.5 V	Х	Х	Х	IN	OUT				

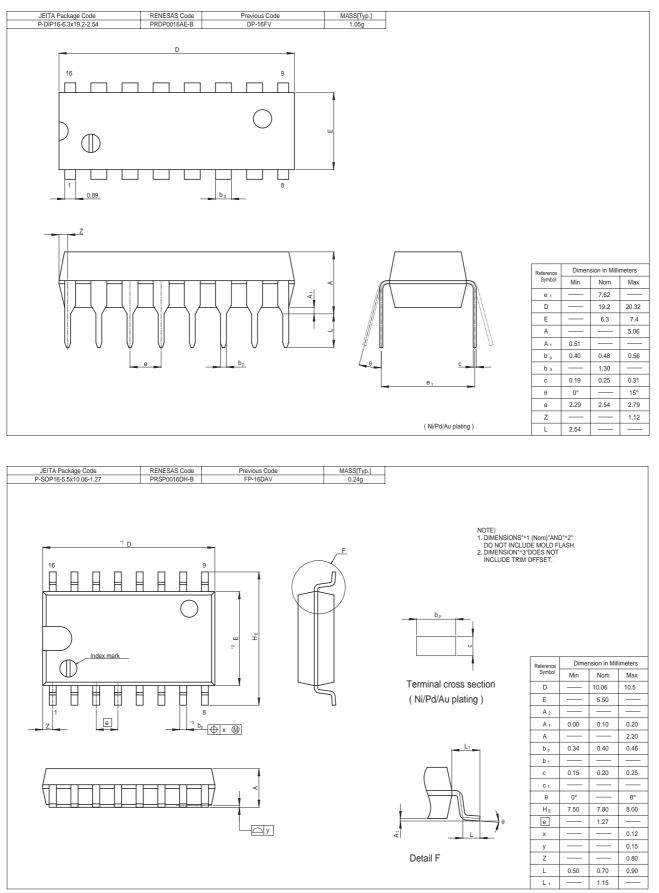
X : "4.5 V" or "GND"

Waveform



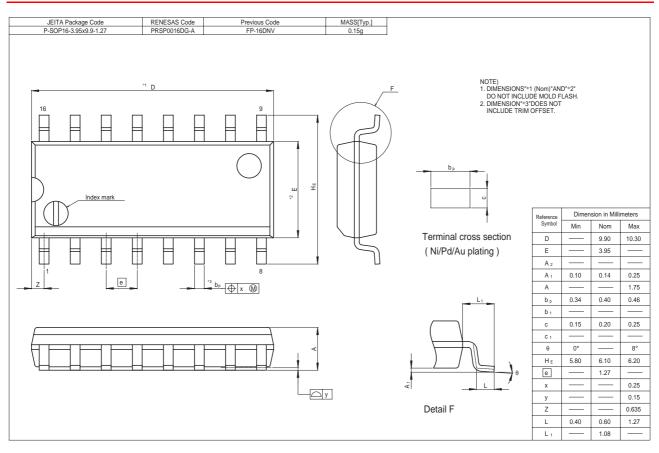


Package Dimensions





HD74LS153





Renesas Technology Corp. Sales Strategic Planning Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan

Keep safety first in your circuit designs! 1. Renesas Technology Corp. puts the maximum effort into making semiconductor products better and more reliable, but there is always the possibility that trouble may occur with them. Trouble with semiconductors may lead to personal injury, fire or property damage. Remember to give due consideration to safety when making your circuit designs, with appropriate measures such as (i) placement of substitutive, auxiliary circuits, (ii) use of nonflammable material or (iii) prevention against any malfunction or mishap.

Notes regarding these materials

- Notes regarding these materials
 1. These materials are intended as a reference to assist our customers in the selection of the Renesas Technology Corp. product best suited to the customer's application; they do not convey any license under any intellectual property rights, or any other rights, belonging to Renesas Technology Corp. ar a third party.
 2. Renesas Technology Corp. assumes no responsibility for any damage, or infringement of any third-party's rights, originating in the use of any product data, diagrams, charts, programs, algorithms, or circuit application examples contained in these materials.
 3. All information contained in these materials, including product data, diagrams, charts, programs and algorithms represents information on products at the time of publication of these materials, and are subject to change by Renesas Technology Corp. without notice due to product improvements or other reasons. It is therefore recommended that customers contact Renesas Technology Corp. or an authorized Renesas Technology Corp. product distributor for the latest product information before purchasing a product listed herein.
 The information described here may contain technical inaccuracies or typographical errors. Renesas Technology Corp. assumes no responsibility for any damage, liability, or other loss rising from these inaccuracies or errors. Please also pay attention to information before making a final decision on the applicability of the information and products. Renesas Technology Corp. Semiconductor home page (http://www.renesas.com).
 4. When using any or all of the information contained in these materials, including product data, diagrams, charts, programs, and algorithms, please be sure to evaluate all information actual system before making a final decision on the applicability of the information and products. Renesas Technology Corp. assumes no responsibility for any damage or manufactured for use in a device or system that is used under circumstanc

- use. 6. The prior written approval of Renesas Technology Corp. is necessary to reprint or reproduce in whole or in part these materials. 7. If these products or technologies are subject to the Japanese export control restrictions, they must be exported under a license from the Japanese government and cannot be imported into a country other than the approved destination. Any diversion or reexport contrary to the export control laws and regulations of Japan and/or the country of destination is prohibited. 8. Please contact Renesas Technology Corp. for further details on these materials or the products contained therein.



RENESAS SALES OFFICES

Refer to "http://www.renesas.com/en/network" for the latest and detailed information.

Renesas Technology America, Inc. 450 Holger Way, San Jose, CA 95134-1368, U.S.A Tel: <1> (408) 382-7500, Fax: <1> (408) 382-7501

Renesas Technology Europe Limited Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K. Tel: <44> (1628) 585-100, Fax: <44> (1628) 585-900

Renesas Technology Hong Kong Ltd. 7th Floor, North Tower, World Finance Centre, Harbour City, 1 Canton Road, Tsimshatsui, Kowloon, Hong Kong Tel: <852> 2265-6688, Fax: <852> 2730-6071

Renesas Technology Taiwan Co., Ltd. 10th Floor, No.99, Fushing North Road, Taipei, Taiwan Tel: <886> (2) 2715-2888, Fax: <886> (2) 2713-2999

Renesas Technology (Shanghai) Co., Ltd. Unit2607 Ruijing Building, No.205 Maoming Road (S), Shanghai 200020, China Tel: <86> (21) 6472-1001, Fax: <86> (21) 6415-2952

Renesas Technology Singapore Pte. Ltd. 1 Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632 Tel: <65> 6213-0200, Fax: <65> 6278-8001

http://www.renesas.com