

# HD74LS32

Quadruple 2-input Positive OR Gate

REJ03D0405-0200 Rev.2.00 Feb.18.2005

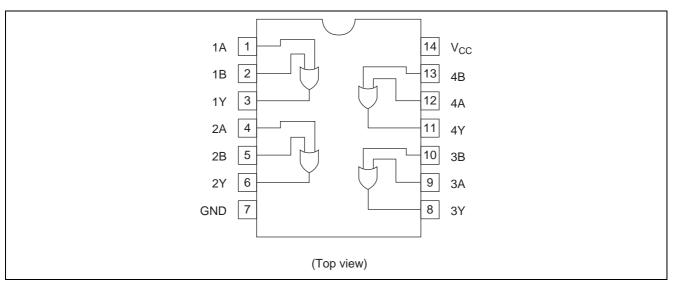
### Features

• Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LS32P	DILP-14 pin	PRDP0014AB-B (DP-14AV)	Р	—
HD74LS32FPEL	SOP-14 pin (JEITA)	PRSP0014DF-B (FP-14DAV)	FP	EL (2,000 pcs/reel)

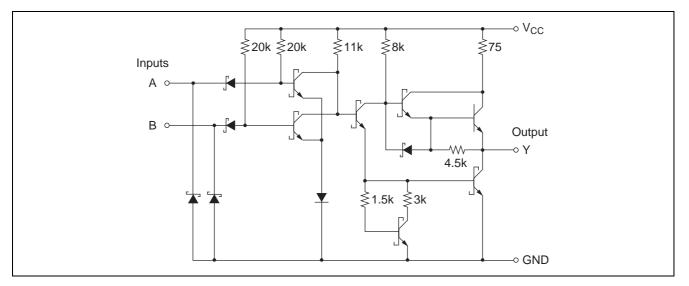
Note: Please consult the sales office for the above package availability.

### **Pin Arrangement**





### **Circuit Schematic (1/4)**



## **Absolute Maximum Ratings**

Item	Symbol	Ratings	Unit
Supply voltage	V <sub>CC</sub>	7	V
Input voltage	V <sub>IN</sub>	7	V
Power dissipation	PT	400	mW
Storage temperature	Tstg	-65 to +150	°C

Note: Voltage value, unless otherwise noted, are with respect to network ground terminal.

# **Recommended Operating Conditions**

Item	Symbol	Min	Тур	Max	Unit
Supply voltage	Vcc	4.75	5.00	5.25	V
Output current	I <sub>OH</sub>	—	—	-400	μA
Output current	I <sub>OL</sub>	—	—	8	mA
Operating temperature	Topr	-20	25	75	°C



### **Electrical Characteristics**

 $(Ta = -20 \text{ to } +75 \ ^{\circ}\text{C})$ 

Symbol	min.	typ.*	max.	Unit	Condition
V <sub>IH</sub>	2.0	—	—	V	
V <sub>IL</sub>	—	—	0.8	V	
V <sub>OH</sub>	2.7	—	—	V	$V_{CC} = 4.75 \text{ V}, V_{IH} = 2 \text{ V}, I_{OH} = -400 \mu\text{A}$
V <sub>OL</sub>	_	—	0.5	V	$I_{OL} = 8 \text{ mA}$ $V_{CC} = 4.75 \text{ V}, \text{ V}_{IL} = 0.8 \text{ V}$
	_	—	0.4		$I_{OL} = 4 \text{ mA}$ $V_{CC} = 4.73 \text{ V}, \text{ V}_{IL} = 0.8 \text{ V}$
I <sub>IH</sub>	—	—	20	μΑ	$V_{CC} = 5.25 \text{ V}, \text{ V}_{I} = 2.7 \text{ V}$
IIL	—	—	-0.4	mA	$V_{CC} = 5.25 \text{ V}, \text{ V}_{I} = 0.4 \text{ V}$
I <sub>I</sub>	—	—	0.1	mA	$V_{CC} = 5.25 \text{ V}, \text{ V}_{I} = 7 \text{ V}$
los	-20	—	-100	mA	V <sub>CC</sub> = 5.25 V
Іссн	—	3.1	6.2	mA	V <sub>CC</sub> = 5.25 V
ICCL	_	4.9	9.8	mA	V <sub>CC</sub> = 5.25 V
VIK	_	_	-1.5	V	$V_{CC} = 4.75 \text{ V}, \text{ I}_{IN} = -18 \text{ mA}$
	VIH VIL VOH VOL IIH IIL II IOS ICCH ICCL	VIH         2.0           VIL         —           VOH         2.7           VOL         —           IIH         —           III         —           IOS         -20           ICCH         —           ICCL         —	$\begin{array}{c cc cc c} V_{IH} & 2.0 & & \\ \hline V_{IL} & & & \\ \hline V_{OH} & 2.7 & & \\ \hline V_{OL} & & & \\ \hline & & & \\ \hline & &$	$\begin{array}{c c c c c c c c c c c c c c c c c c c $	$ \begin{array}{c c c c c c c c c c c c c c c c c c c $

Note:  $* V_{CC} = 5 V$ , Ta =  $25^{\circ}C$ 

### **Switching Characteristics**

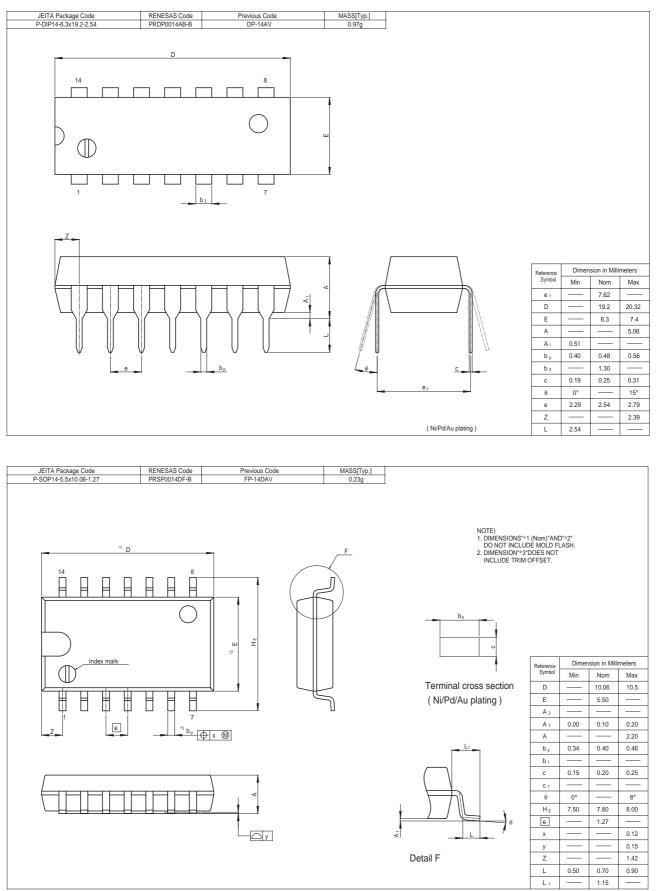
$(V_{CC} = 5 V)$	, Ta =	25°C)
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ltem	Symbol	min.	typ.	max.	Unit	Condition	
Propagation delay time	t <sub>PLH</sub>	_	14	22	ns	$C_1 = 15 \text{ pF}, R_1 = 2 \text{ k}\Omega$	
	t <sub>PHL</sub>	_	14	22	ns	$C_{L} = 15 \text{ pr}, R_{L} = 2 \text{ K}\Omega$	

Note: Refer to Test Circuit and Waveform of the Common Item "TTL Common Matter (Document No.: REJ27D0005-0100)".



### **Package Dimensions**





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